



Mahavir Education Trust's
Shah & Anchor Kutchhi Engineering College, Chembur, Mumbai 40088

UG Programme in Electronics Engineering

DATE: 5/4/2022

TIME TABLE-INTERNAL ASSESSMENT II (TE, BE)

| Time | Date | TE | BE |
|---------------------|-----------|---------------------------------------|--|
| 12:15 PM TO 1:30 PM | 22-4-2022 | Basic VLSI (BVLSI) | Internet of Things (IoT) |
| 3:45 PM TO 5:00 PM | 22-4-2022 | Electromagnetic Engineering (EEM) | |
| 12:15 PM TO 1:30 PM | 25-4-2022 | Computer Communication Networks (CCN) | Analog and Mixed VLSI (AVLSI) |
| 12:15 PM TO 1:30 PM | 27-4-2022 | Embedded Systems & RTOS (ESRTOS) | MEMS Technology (MEMS)/ Digital Image Processing (DIP) |
| 3:45 PM TO 5:00 PM | 27-4-2022 | Machine Learning (ML) | Environmental Management (EVM)/Project Management (PM) |

S. Subha S.
HOD: Dr. Subha S.

A. Arana
COE: Prof. Arana Sharma

B. B. Patel
PRINCIPAL: Dr. Bhavesh Patel



Mahavir Education Trust's
Shah & Anchor Kutchhi Engineering College, Chembur, Mumbai 40088

UG Programme in Electronics and Computer Science

DATE:05/04/2022

TIME TABLE- IA 2 SE Sem4

| Time | Date | SE |
|---------------------|-----------|-------------------------------------|
| 12:15 PM TO 1:30 PM | 22-4-2022 | Engineering mathematics -IV |
| 3:45 PM TO 5:00 PM | 22-4-2022 | Electronic Circuits |
| 12:15 PM TO 1:30 PM | 25-4-2022 | Control and Instrumentation |
| 12:15 PM TO 1:30 PM | 27-4-2022 | Microprocessor and Microcontrollers |
| 3:45 PM TO 5:00 PM | 27-4-2022 | Discrete Structure Automata Theory |

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