



Mahavir Education Trust's
Shah & Anchor Kutchhi Engineering College, Chembur,
Mumbai 400088

UG Programme in Electronics Engineering
TIME TABLE FOR INTERNAL ASSESSMENT II

DATE: 20/04/2021

Time	Date	SE	TE	BE
11:30 AM TO 12 PM	3-05-2021	Applied Mathematics IV	Embedded Systems & Real Time Operating System	-
12:30 AM TO 1:00 PM	3-05-2021	-	-	Internet of Things
2:30 PM TO 3 :00 PM	3-05-2021	Electronics Devices & Circuit II	Computer Communication and Networks	Analog and Mixed VLSI
11:30 AM TO 12 PM	4-05-2021	Micro-Controller Applications	VLSI Design	-
2:30 PM TO 3 :00 PM	4-05-2021	-	-	IPR and Patenting / Environment Management / Project Management
11:30 AM TO 12 PM	5-05-2021	Principles of Communication Engineering	(Elective Subject) WCOM/ COA	MEMS Technology / Advanced Power Electronics / Digital Image Processing
2:30 PM TO 3 :00 PM	5-05-2021	Signal and Systems	Signal and Systems	-

HOD: Dr. Subha S.

COE: Prof. Aruna Sharma

PRINCIPAL: Dr. Bhavesh Patel