



Mahavir Education Trust's
Shah & Anchor Kutchhi Engineering College, Chembur,
Mumbai 400088

UG Programme in Electronics Engineering
JS & SS Duty Chart for IA2-DSE

DATE: 24/03/2021

Date	Time	SE	ME	SS
01-04-2021	11:30 am to 12:00 pm	Engineering Mathematics III(Seema B)	Advance Digital communication(Prashant Khedkar)	Shubha S.
	2:30 pm to 3:00 pm	Electronics Devices & Circuit I (Jayshree G.)	Power Electronics System Design(Asha Durafe)	
03-04-2021	11:30 am to 12:00 pm	Digital Logic Circuits (Manjusha K.)	Mixed Signal VLSI Design(Shubha Subramanium)	Babychen M.
	2:30 pm to 3:00 pm	Electrical Network Analysis and Synthesis (Nandkishor N.)	Cyber Securities and Laws(Vidya Gogate)	
05-04-2021	11:30 am to 12:00 pm	-	-	Vidya G.
	2:30 pm to 3:00 pm	Electronics Instruments and measurements (Pooja P.)	Advanced Processor Architecture(Shubha Subramanium)	

HOD: Sulha Sul
24/3/2021

Ashara
COE:

Bo
PRINCIPAL: