



Mahavir Education Trust's  
Shah & Anchor Kutchhi Engineering College, Chembur,  
Mumbai 400088

**UG Programme in Electronics Engineering**  
**TIME TABLE FOR INTERNAL ASSESSMENT I**

DATE: 24/02/2021

Date	Time	SE	TE	BE	ME
10-03-2021	11:30 am to 12:00 pm	Engineering Mathematics IV	Embedded Systems & Real Time Operating System	Internet of Things	Advance Digital communication
	2:30 pm to 3:00 pm	Electronics Devices & Circuit II	Computer Communication and Networks	Analog and Mixed VLSI	Power Electronics System Design
12-03-2021	11:30 am to 12:00 pm	Microcontroller Applications	VLSI Design	-	Mixed Signal VLSI Design
	2:30 pm to 3:00 pm	-	-	IPR and Patenting/ Environment Management/Project Management	-
13-03-2021	11:30 am to 12:00 pm	Principles of Communication Engineering	Elective Subject (W.COM/COA)	-	Cyber Securities and Laws
	2:30 pm to 3:00 pm	Signal and Systems	Signals and Systems	MEMS Technology/ Advanced power electronics/ Digital Image Processing	Advanced Processor Architecture

HOD:

COE:

PRINCIPAL:

*Sul Gul*  
24/2/2021

*A. Sharma*

*Ba*

*IAC*  
*Prady*  
24/2/2021  
*ABU*  
24/2/21



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**TIME TABLE FOR INTERNAL ASSESSMENT I-DSE**

DATE: 24/02/2021

Date	Time	SE
17-03-2021	11:30 am to 12:00 pm	Engineering Mathematics III
	2:30 pm to 3:00 pm	Electronics Devices & Circuit I
18-03-2021	11:30 am to 12:00 pm	Digital Logic Circuits
	2:30 pm to 3:00 pm	Electrical Network Analysis and Synthesis
19-03-2021	11:30 am to 12:00 pm	-
	2:30 pm to 3:00 pm	Electronics Instruments and measurements

HOD :

*Sulth Sul*  
*24/2/2021*

COE:

*Shane*

PRINCIPAL:

*Pr*

F.A.C.

*Mundy*  
*24/2/2021*

*Arav*  
*24/2/21*